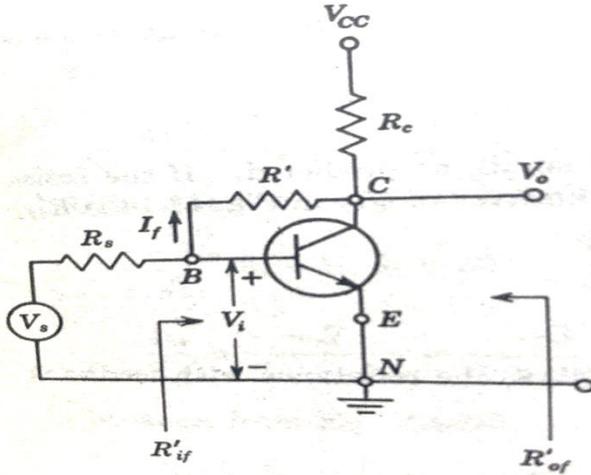


Name:			
Enrolment No:			
UPES End Semester Examination, May,2023			
Course:Analog Electronics-II Program: B.Tech ECE Course Code:ECEG 2014		Semester: IV Time : 03 hrs. Max. Marks: 100	
Instructions:			
SECTION A (5Qx4M=20Marks)			
S. No.		Marks	CO
Q 1	Explain with the help of a schematic diagram the operation of a single loop feedback amplifier	4	CO2
2	Find the frequency of operation of a phase shift oscillator.	4	CO1
3	Explain the frequency response of a crystal	4	CO1
4	Define the output offset voltage and input offset current of opamp.	4	CO4
5	Find the expression for power output for a large signal class A amplifier.	4	CO3
SECTION B (4Qx10M= 40 Marks)			
Q 6	Find the expression for second harmonic distortion for a large signal Class A amplifier	10	CO3
7	How impedance matching is performed in the transformer coupled audio power amplifier? Find the expression for conversion efficiency for such amplifier.	10	CO3
8	a) Design a low pass filter at a cutoff frequency of 1 KHz at a pass band gain of 2. b) Using IC 741 , design an noninverting amplifier with three inputs for acting as an averaging amplifier.	10	CO4
9	Explain a differential instrumentation amplifier using a transducer bridge. How will you convert the above circuit into a temperature controller?	10	CO4

SECTION-C
(2Qx20M=40 Marks)

Q 10

The circuit diagram given below has the following parameters:
 $R_e=4k$, $R'=40k$, $R_s=10k$, $h_{ie}=1.1k$, $h_{fe}=50$, and $h_{re}=h_{oe}=0$.
 What type of feedback is this? Find
 a) A_{vf}
 b) R_{if}
 c) R_{of}



20

CO2

Q11

- a) Design a square wave oscillator so that $f_0=1$ KHz. Select a 741 opamp with DC supply voltages ± 15 V.
 b) Explain a successive approximation analog to digital converter.

OR

- a) Design a triangle wave generator so that $f_0=2$ KHz and $v_0(pp)=7$ V. The Opamp is a 1458/772 and supply voltage = ± 15 V.
 b) Describe a Monostable 555 timer and find the expression for time constant.

20

CO4