

Name:
Enrolment No:



UNIVERSITY OF PETROLEUM AND ENERGY STUDIES
End Semester Examination, May 2020

Course: Resource allocation on Mainframe
Program: B.Tech. (CSE) with Spl. in Mainframe Technology
Course Code: CSIB462

Semester: VIII
Time: 03 hrs.
Max. Marks: 100

Instructions:

SECTION A

S. No.		Marks	CO
Q 1	i. _____ is a tool through which the hardware configuration can be specified to the OS and the Channel systems. ii. A _____ topology will have a single communication line with a controller at one end and the terminator at the other end and the nodes in between.	2.5*2 =5M	CO1
Q 2	i. The Support Element is located inside the same frame that the Central Processor Complex (CPC) is located. (True/ False) ii. The HMC's primary function is to provide a console for system administrators and service personnel to manage server hardware. (True/ False)	2.5*2 =5M	CO2
Q 3	i. The HMC is delivered with its machine code preinstalled on the disk drive. (True/ False) ii. HMC is composed of many subsystems and layers of code. (True/ False)	2.5*2 =5M	CO2
Q 4	i. ESCON channel protocol is faster than FICON. (True/ False) ii. Which one is not a characteristic of LED? a. Low Power b. Low cost c. Single light wavelength d. none of these	2.5*2 =5M	CO3
Q 5	i. Validation of the configuration data specified in the IOCDs is carried out by the MVSCP until the resource is accessed. ii. HCD also has an _____ through which it allows search and update in IODF.	2.5*2 =5M	CO4
Q 6	i. _____ is used to dynamically add, remove or modify LPAR resources such as memory, CPU, or I/O devices. ii. HMC provides panel oriented navigation techniques to prevent errors.(True/False)	2.5*2 =5M	CO2

SECTION B

Q 7	Illustrate the use of logical partitioning. Which mode of CPC you will use out of basic and LPAR mode? Justify your answer.	10M	CO1
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Q 8	i. Compare Static, dynamic and HyperPAV. ii. Differentiate Asynchronous transfer mode vs Wavelength division multiplexing	10M	CO1
Q 9	Describe different phases of problem analysis framework.	10M	CO2
Q 10	Explain the procedure of deleting of partitions with CHIPIDs uniquely defined.	10M	CO4
Q 11	i. Illustrate the use of IPL in Mainframe. (OR) ii. Compare Cold start, Quick start, and Warm start.	10 M	CO4
SECTION-C			
Q 12	i. Justify, can a tape device be automatically switchable. ii. Illustrate the process for managing IOCDS and IPL attributes across a Sysplex (OR) iii. Justify, can an I/O device be defined dynamically? iv. Write different performance tests for the Operating system and network layers under the HMC performance test.	10M*2= 20M	CO3